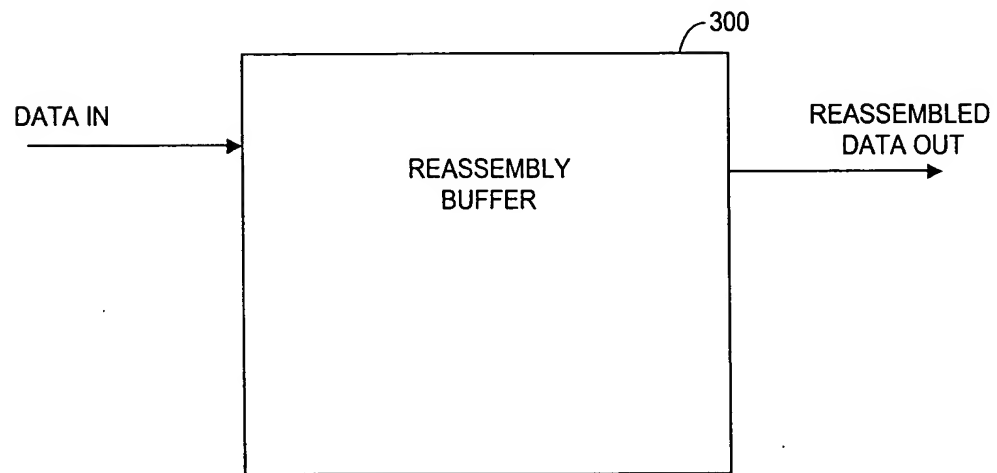
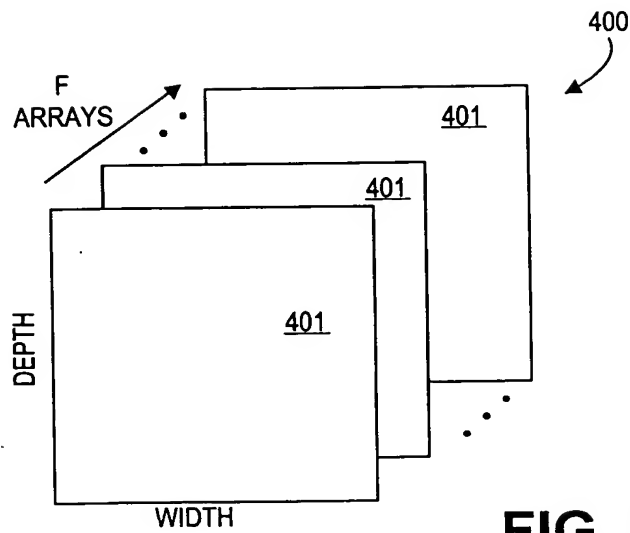
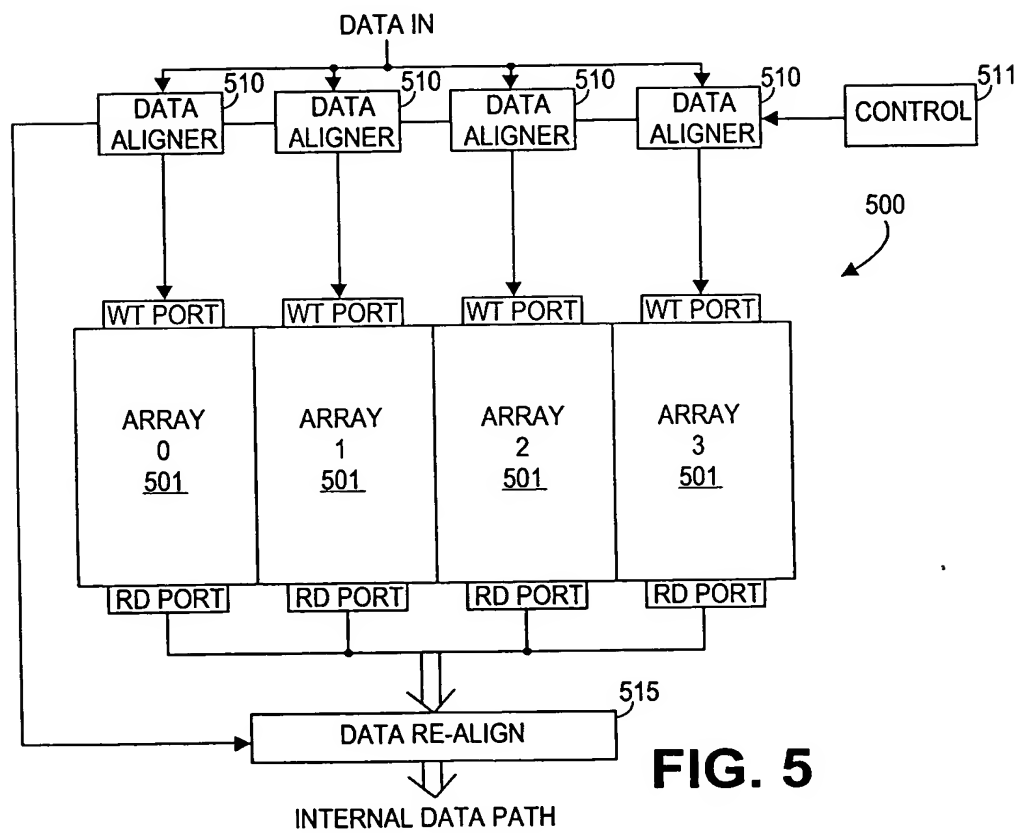
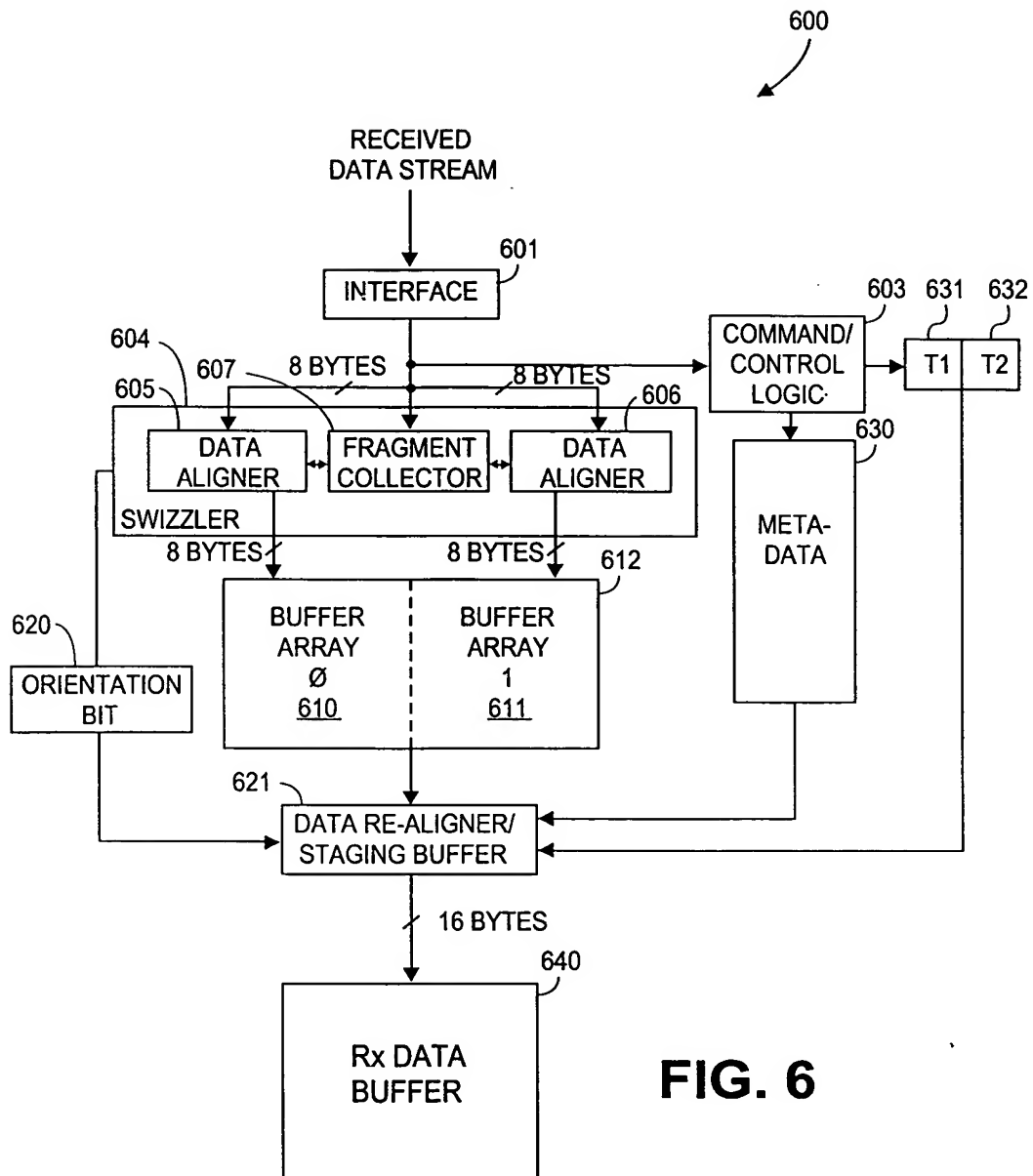
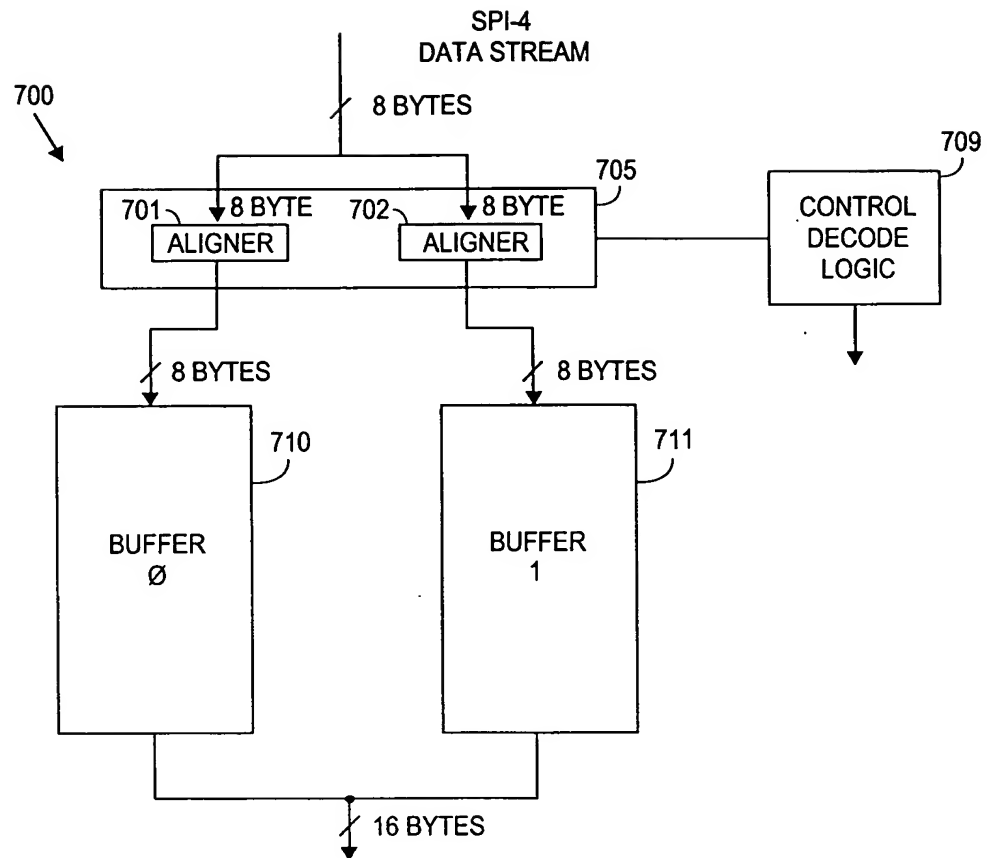
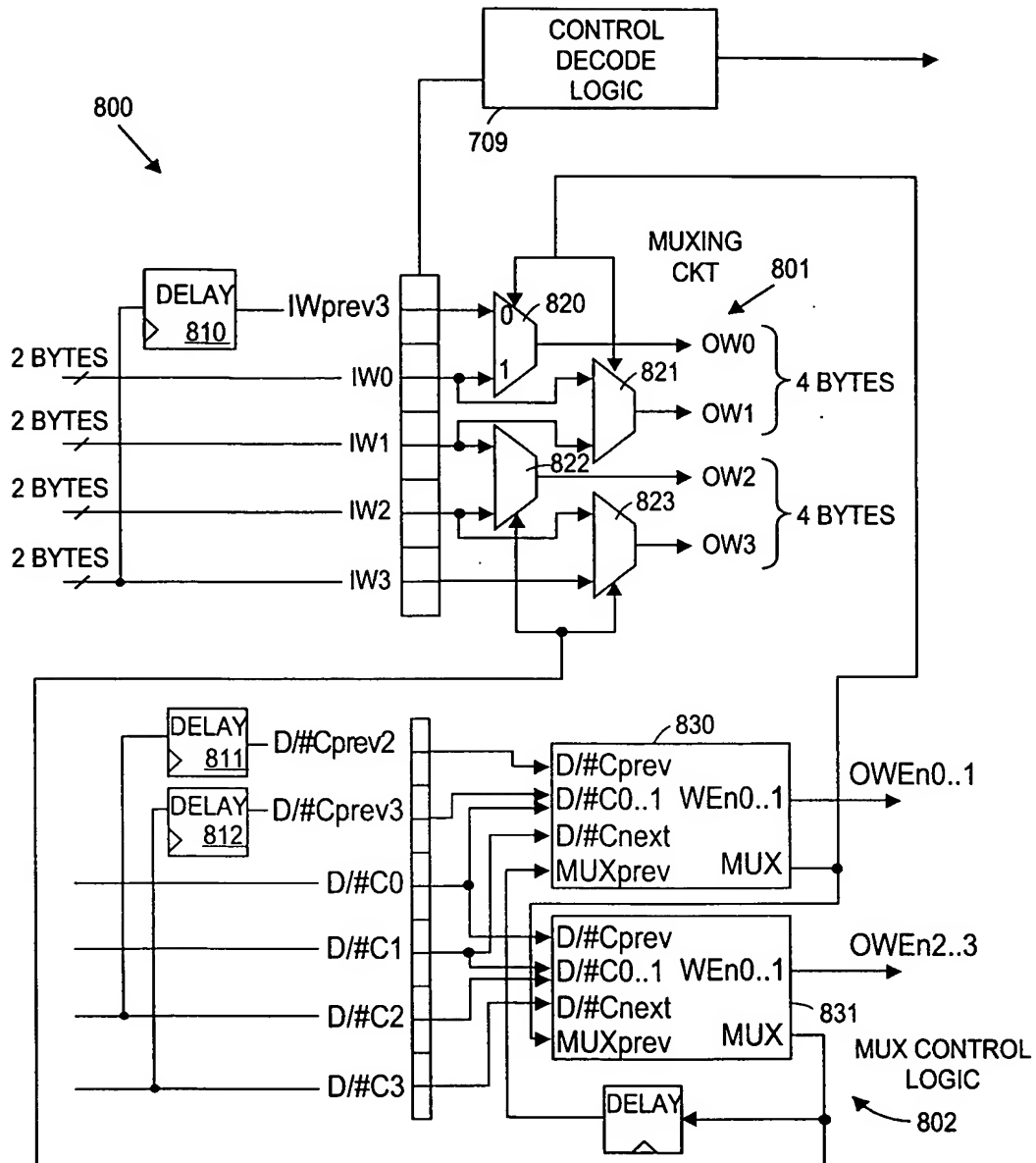
**FIG. 2****FIG. 3**

**FIG. 4****FIG. 5**



**FIG. 7**

**FIG. 8**

900

D#C0..1	D#Cprev	MUXprev	D/#Cnext	IW is	POS enter	MUX	WEn0..1	
11	0	x	x	C-01-x	0	0	11	STATE MACHINE OUTPUT
11	1	0	x	1-01-x	++	0	11	
11	1	1	1	0-10-1	++	1	11	
11	1	1	0	0-10-C	++	1	10	
10	0	x	x	C-0C-x	0	0	10	
10	1	0	x	1-0C-x	++	0	10	
10	1	1	x	0-1C-x	x	x	00	
01	x	x	1	x-C0-1	0	1	11	
01	x	x	0	x-C0-C	0	1	10	
00	x	x	x	x-CC-x	x	x	00	
STATE MACHINE INPUT				DATA WORD STATE	Position of the double-byte in the packet after a control			
				0=even double-byte 1=odd double-byte C=control				

FIG. 9